EP93xx RTC Oscillator Circuit

Note: This application note is applicable to the D1, E0 and E1 revisions of the chip. If your application uses the D1 or E0 revision of the chip, you will also need to implement the changes detailed in application note AN258. To determine the chip revision, see "How to Determine the Silicon Revision of the Integrated Circuit" on page 4.

1. BACKGROUND

Cirrus Logic has found that the Real Time Clock (RTC) circuit in the current EP93xx chips is susceptible to on-chip noise, which can generate an inaccurate clock count and possibly cause the IC to boot into an improper state during power-up.

To correct this issue, customers need to provide a clean square wave input to the RTCXTALI pin. This may be accomplished several ways, such as by using an external clock oscillator or a dedicated RTC chip like the DS1337. This application note, however, shows the use of an external Pierce oscillator circuit to replace the existing internal RTC circuit.

Please note that an external RTC oscillator circuit is required for the EP93xx family on both existing and future revision parts.

2. IMPLEMENTATION

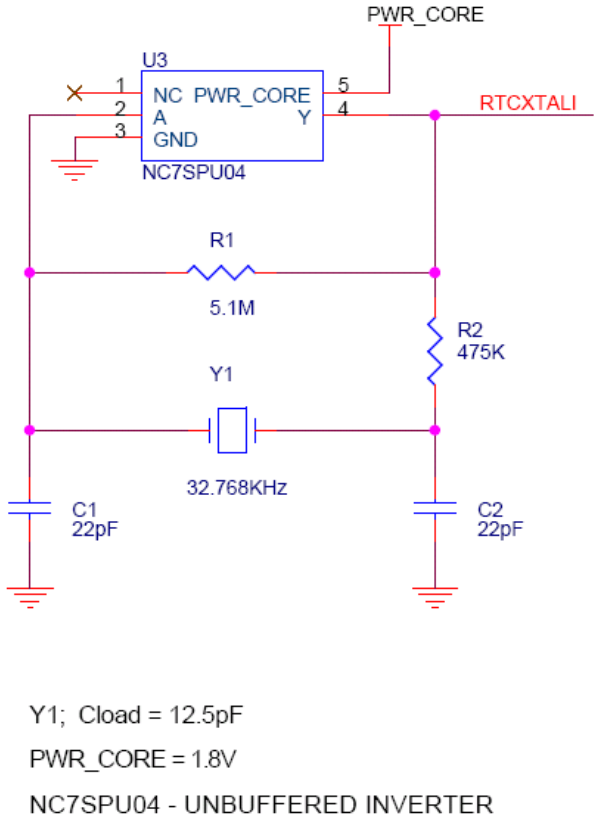

Figure 1. Implementation Using a Pierce Oscillator Circuit

The NC7SPU04 is an unbuffered inverter that is powered from the 1.8 VDC rail. The output of the NC7SPU04 connects to the RTCXTALI input on the EP93xx device. The RTCXTALO pin is left open. The exact value of the capacitors may need to be adjusted based on the actual crystal used and the layout and routing of the circuit. Care should be taken to minimize the trace lengths and to avoid high speed signals near the oscillator input. The 475 kΩ resistor (R2) ensures that the crystal is not overdriven. Overdriving the crystal can lead to premature aging and failure of the crystal. The RTC clock is vital to booting up the EP93xx devices. Without an RTC clock, the EP93xx processor will not boot.

The oscilloscope pictures below show the output of the oscillator circuit (Figure 2.), as well as rising edge slew (Figure 3.), falling edge slew (Figure 4.) and start up (Figure 5.).

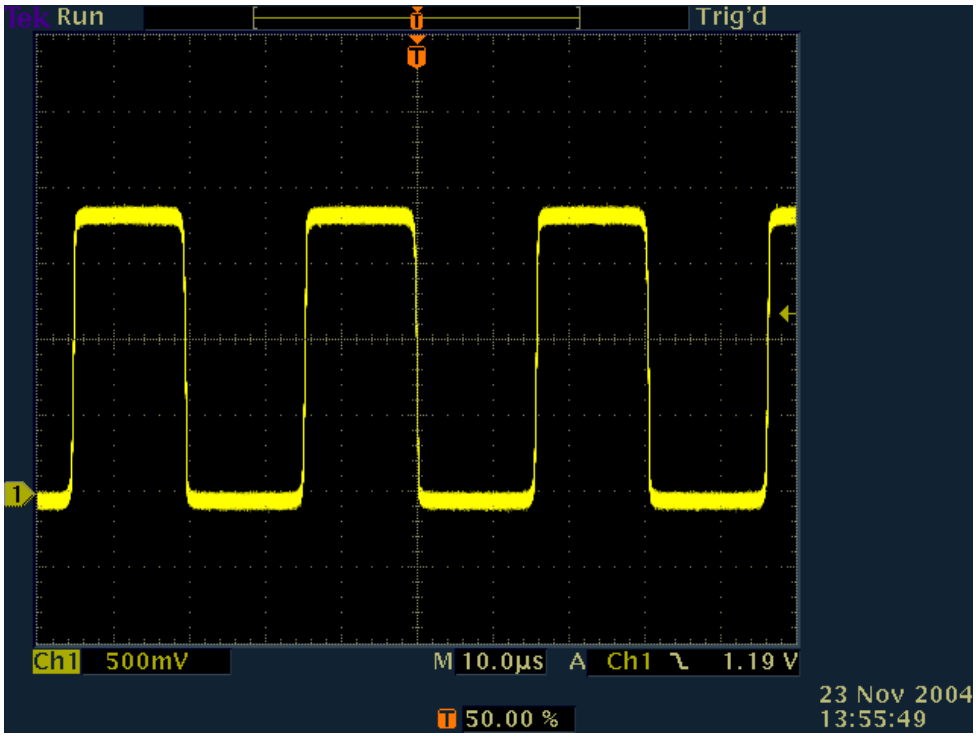

Figure 2. RTC Oscillator Output.

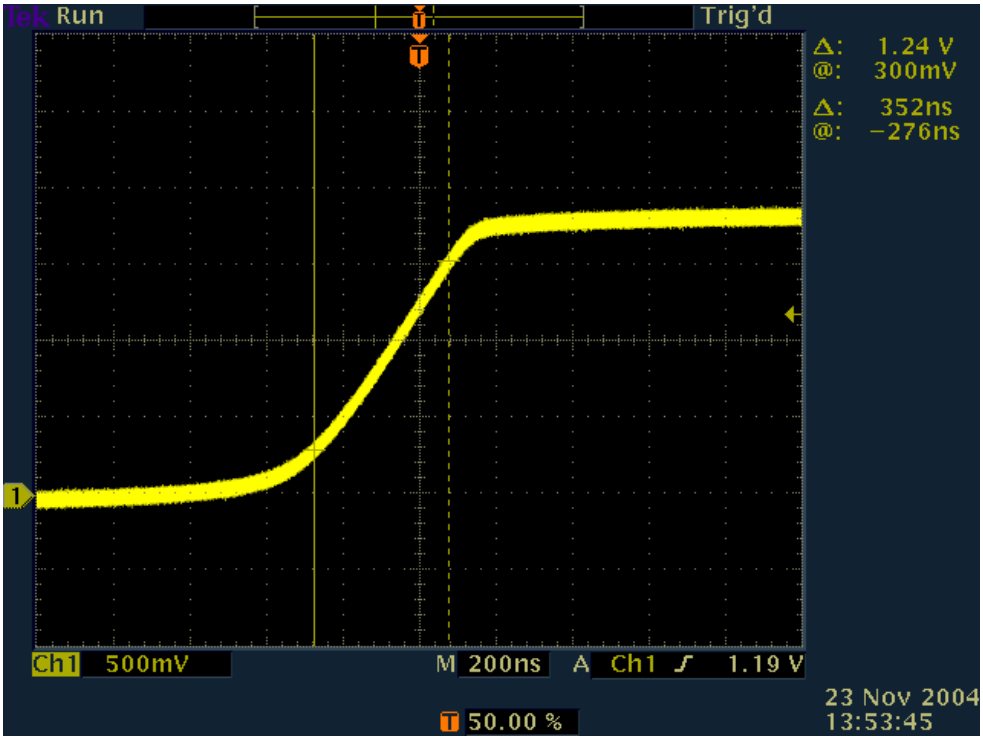

Figure 3. RTC Oscillator Output - Rising Edge.

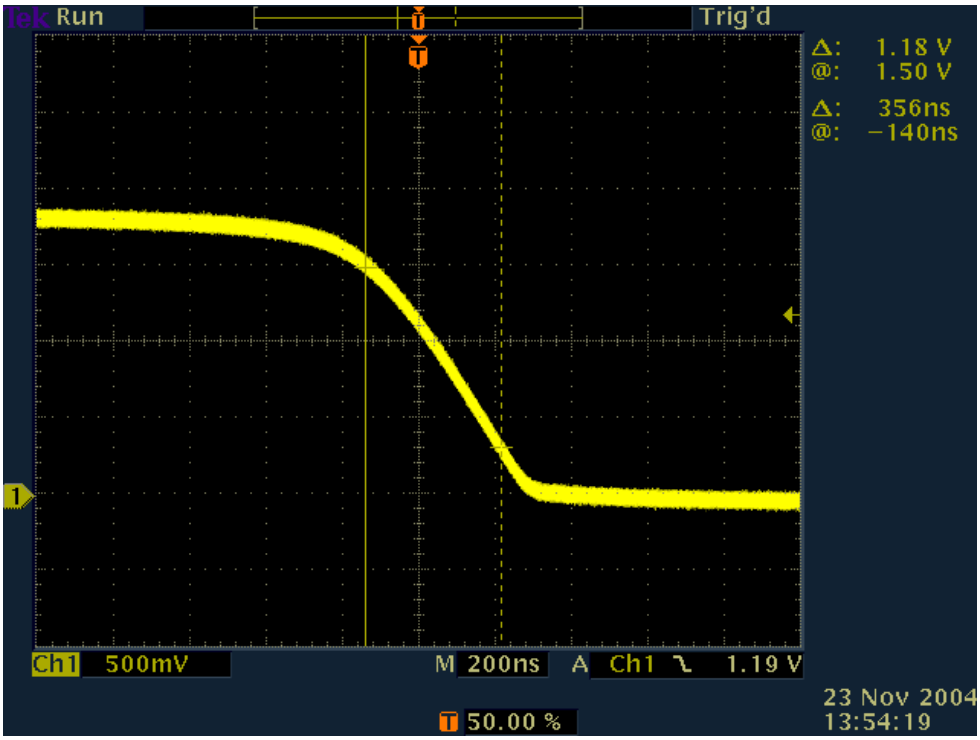

Figure 4. RTC Oscillator Output - Falling Edge.

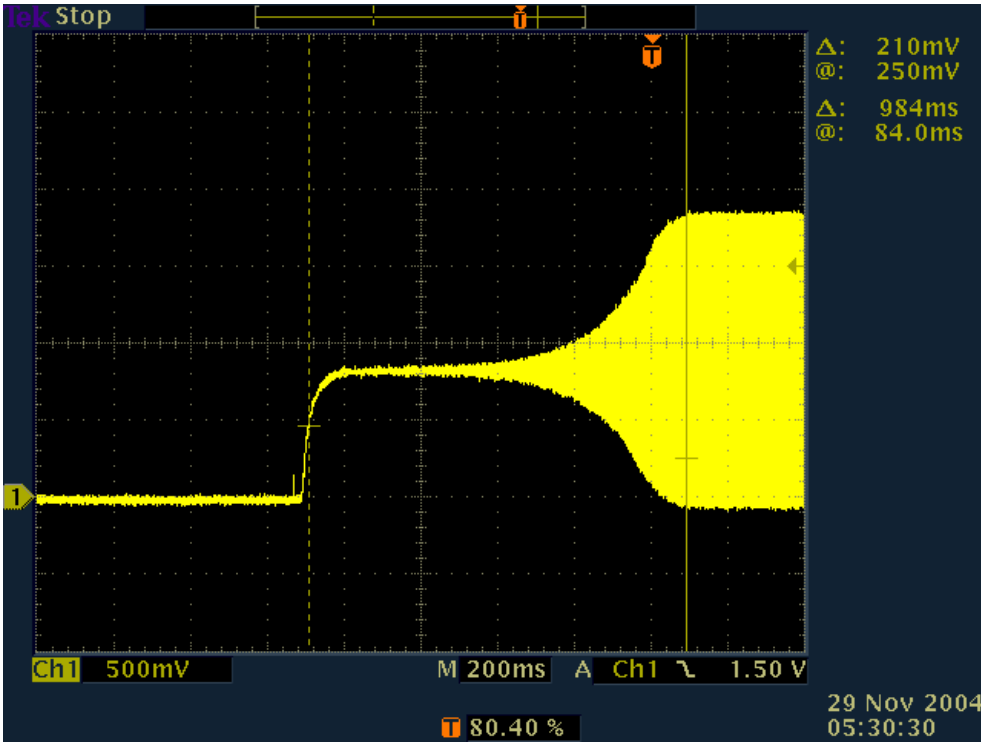

Figure 5. RTC Oscillator Startup.

How to Determine the Silicon Revision of the Integrated Circuit

On the front of the integrated circuit, directly under the part number, is an alphanumeric line. Characters 5 and 6 in this line represent the silicon revision of the chip. For example, this line indicates that the chip is a “E0” revision chip:

EFWA**E0**AM0340

Table 1. Revision History

Release	Date	Changes
1	November 2004	Initial Release

Contacting Cirrus Logic Support

For a complete listing of Direct Sales, Distributor, and Sales Representative contacts, visit the Cirrus Logic web site at:
http://www.cirrus.com/corporate/contacts